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FS-K320QVB-V1 24/Sep/2007

1. Document revision history :							
DOCUMENT REVISION	DATE	DESCRIPTION	PREPARED BY	APPROVED BY			
REVISION	2007.09.24	First Release.	BY Van Ng	BY			

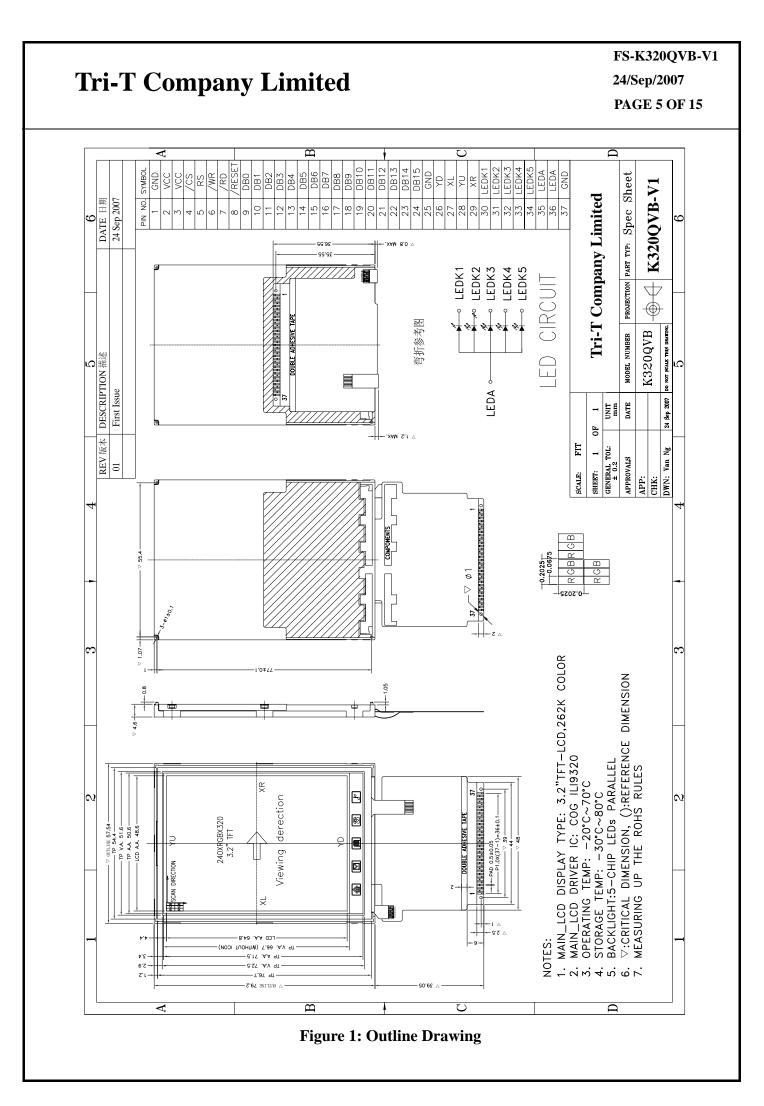
2. General Description

- 3.2"(diagonal), 240 x RGB x 320 dots, 262k colors, Transmissive, TFT LCD module.
- Viewing Direction: 9 o'clock.
- Driving IC: ILI9320 or equivalent TFT controller/driver.
- 16-bits data bus (I80 system interface).
- Logic voltage: 2.8V (typ.).
- Touch panel.

3. Mechanical Specifications

The mechanical detail is shown in Fig. 1 and summarized in Table 1 below.

Table 1						
Pa	rameter	Specifications	Unit			
Outline	dimensions	57.54(W) x 79.2(H) x 4.6(D) (Exclude FPC, cables of touch panel and backlight)	mm			
	View area	51.6(W) x 72.5(H)	mm			
	TP active area	50.6(W) x 71.5(H)	mm			
Color TFT	LCD active area	48.6(W) x 64.8H)	mm			
240xRGBx320	Display format	240 x RGB x 320	dots			
	Color configuration	RGB stripes	-			
	Dot pitch	0.2025(RGB)(W) x 0.2025(H)	mm			
Weight		TBD	grams			



4. Interface signals

	Table 2. Die aggionment					
<u> </u>		Table 2: Pin assignment				
Pin No.	Symbol	Description				
1	GND	Ground for the logic and analog circuit.				
2	VCC	A power supply for the internal logic circuit and for the I/O circuit.				
3	VCC	$VCC = 2.2 \sim 3.3 V.$				
4	/CS	Chip select signal. 0: chip can be accessed; 1: chip cannot be accessed.				
5	RS	Register Select Signal (H: Data, L: Instruction)				
6	/WR	I80 system: Serves as a write signal and writes data at the rising edge.				
7	/RD	I80 system: Serves as a read signal and reads data at the low level.				
8		Reset pin. Setting either pin low initializes the LSI.				
0	/RESET	Must be reset the chip after power being supplied.				
9-24	[DB5-DB7]	16-bit bi-directional data bus.				
25	GND	Ground for the logic and analog circuit.				
26	YD					
27	XL	Corminal of touch name				
28	YU	Terminal of touch panel.				
29	XR					
30	LEDK1					
31	LEDK2					
32	LEDK3	Cathode of LED backlight.				
33	LEDK4					
34	LEDK5					
35	LEDA	Anoda of LED backlight				
36	LEDA	Anode of LED backlight.				
37	GND	Ground for the logic and analog circuit.				

5. Absolute Maximum Ratings 5.1 **Electrical Maximum Ratings – for IC Only** Table 3: Electrical Maximum Ratings - for IC Parameter Symbol Min. Max. Unit Note Power supply voltage (VDD) VCC -0.3 +4.6V 1 Note: 1.VCC, GND must be maintained. 2. The modules may be destroyed if they are used beyond the absolute maximum ratings. 5.2 **Environmental Condition** Table 4 Storage Operating temperature temperature Remark Item (Tstg) (Topr) (Note 1) Min. Max. Min. Max. Ambient temperature -20°C +70°C -30°C +80°C Dry 80% max. RH for Ta \leq 40°C No Humidity (Note 1) < 50% RH for 40°C < Ta \leq Maximum operating temperature condensation

Note 1: Product cannot sustain at extreme storage conditions for long time.

6. Electrical Specifications

Typical Electrical Characteristics

At Ta = 25 °C, VCC=IOVCC= 2.2V to 3.3V, GND=0V.

		Table 5				
Parameter	Symbol	Conditions	Min.	Тур.	Max.	Unit
Supply voltage (logic)	VDD-GND		2.6	2.8	3.3	V
Supply current (Logic & LCD)	ICC	VDD=2.8V	-	-	10	mA
Supply voltage of white LED backlight	VLED =V(bL+)- V(bL-)	Forward current =75mA Number of LED	2.9	3.2	3.5	V
Luminance (on the module surface)		dies = 5	-	150	-	cd/m ²

7. Optical Characteristics

		<u>Table</u>	e 7: Optical sp	ecifica	<u>itions</u>			
Itoma		Symphol	Condition	Specifications		Unit		
Items		Symbol	Condition	Min.	Тур.	Max.	Unit	
Contrast Ra	atio	CR		-	300	-	-	
Response T	ime	T _R		-	10	20	ms	
Kesponse I	line	$T_{\rm F}$		-	15	20	ms	
	Red	X _R		0.627	0.642	0.657	-	
	Reu	Y _R		0.315	0.330	0.345	-	
	Green	X _G		0.264	0.279	0.294	-	
Chromaticity		Y _G		0.556	0.571	0.586	-	Note
Cinomationy	Blue	X _B		0.121	0.136	0.151	-	noic
		Y _B		0.083	0.098	0.113	-	
	White	X_{W}		0.293	0.308	0.323	-	
	white	Y_W		0.308	0.323	0.338	-	
	Hor.	$\phi 1(3 \text{ o'clock})$		50	60	-		
Viewing angle		$\phi 2(9 \text{ o'clock})$	Center	30	40	-	deg.	
	Ver.	$\theta 2(12 \text{ o'clock})$	CR≥10	50	60	-		
		$\theta 1(6 \text{ o'clock})$		50	60	-		
NTSC ratio					61.5		%	

Note 1: Definition of Contrast Ratio (CR):

The contrast ratio can be calculated by the following expression.

Contrast Ratio (CR) = L63 / L0

L63: Luminance of gray level 63

L0: Luminance of gray level 0

CR = CR (10)

CR (X) is corresponding to the Contrast Ratio of the point X at Figure in Note 5. Note 2: Definition of Response Time (TR, TF):

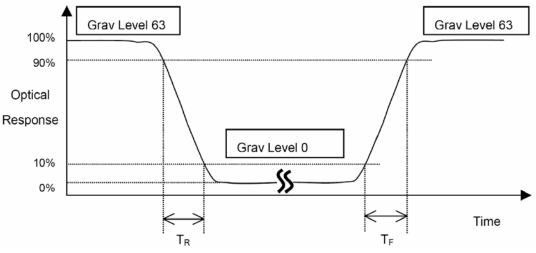


Figure 3

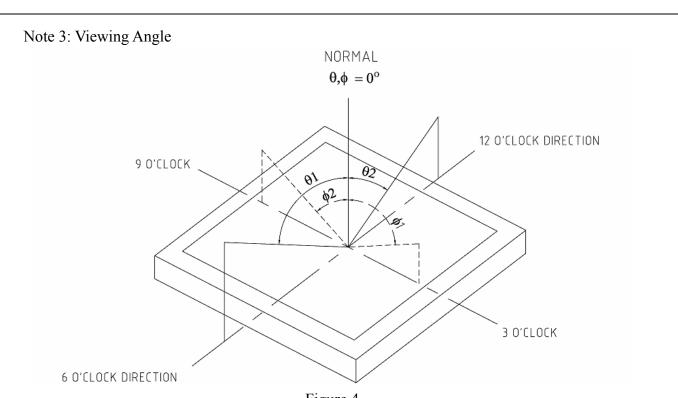
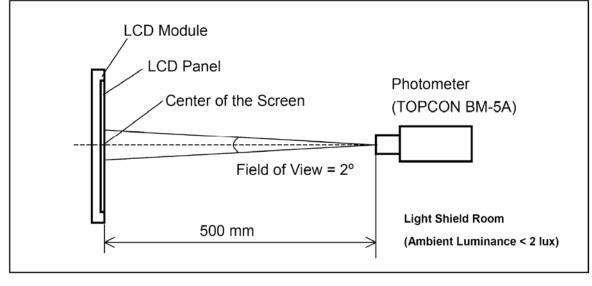


Figure 4

The above "Viewing Angle" is the measuring position with Largest Contrast Ratio; not for good image quality. View Direction for good image quality is 6 O'clock. Module maker can increase the "Viewing Angle" by applying Wide View Film.

Note 4: Measurement Set-Up:

The LCD module should be stabilized at a given temperature for 20 minutes to avoid abrupt temperature change during measuring. In order to stabilize the luminance, the measurement should be executed after lighting Backlight for 20 minutes in a windless room.



8. Timing Characteristics

8.1 80-system Bus Interface Timing Characteristics of IC

Table 8: Normal Write Mode (VCC = $IOVCC=2.4\sim3.3V$)

	Symbol	Unit	Min.	Тур.	Max.		
Rus avala tima	Write	t _{CYCW}	ns	125	-	-	
Bus cycle time	Read	t _{CYCR}	ns	300	-	-	
Write lo	w-level pulse width	PWLW	ns	40	-	-	
Read lo	w-level pulse width	PWLR	ns	150	-	-	
Write hig	gh-level pulse width	PW _{HW}	ns	70	-	-	
Read hig	gh-level pulse width	PW _{HR}	ns	150	-	-	
Write / I	t_{WRr} , t_{WRf}	ns	-	-	25		
RS Setup time	(RS to NCS, NWR)	t _{AS}	ns	5	-	-	
RS hold time	(NCS, NWR to RS)	t _{AH}	ns	5	-	-	
Write	t _{DSW}	ns	20	-	-		
Write	t _H	ns	15	-	-		
Read	t _{DDR}	ns	-	-	100		
Read	t _{DHR}	ns	5	-	-		

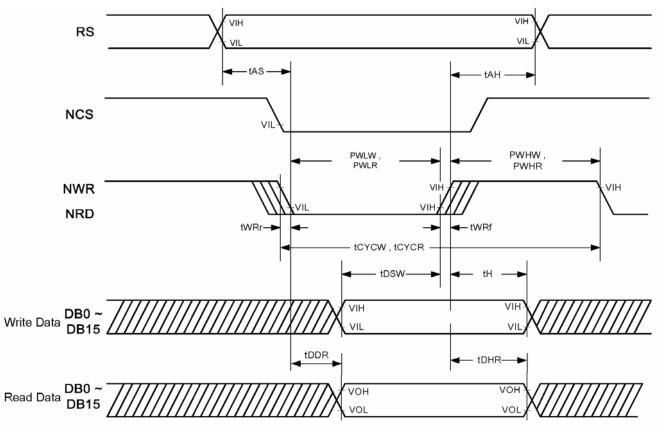


Figure 7. 80-system Bus Timing

8.2 Reset Operation of IC

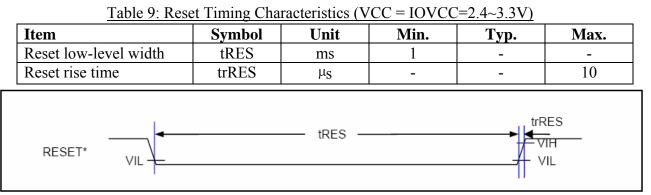


Figure 8: Reset Timing

9. Reliability Test Item

Test Item	Sample Type	Test Condition	Test result determinant gist
High temperature	Normal temperature	70±3°C;96H	the inspection of
storage	Wide temperature	80±3°C;96H	appearance and function
Low temperature	Normal temperature	-20±3°C;120H	character.
storage	Wide temperature	-30±3°C;120H	
High temperature	Normal temperature	50°C±3°C,90%±3%RH;96H	
/humidity storage	Wide temperature	60°C±3°C,90%±3%RH;96H	
High temperature	Normal temperature	60±3°C;96H	no objection of the function
operation	Wide temperature	70±3℃;96H	character; no fatal objection of
Low temperature	Normal temperature	0±3°C;96H	the appearance.
operation	Wide temperature	-20±3°C;96H	
High temperature	Normal temperature	40°C±3°C,90%±3%RH;96H	
/humidity operation	Wide temperature	50°C±3°C,90%±3%RH;96H	
Temperature Shock	Normal temperature	-20±3°C,30min→70±3°C,30 min;10cycle	inspect the objections appearance, function & the whole structure
	Wide temperature	-30±3℃,30min 80±3,30min;10cycle	The inspection of appearance, function & the whole structure

10. Suggestions for using LCD modules

10.1 Handling of LCM

- 1. The LCD screen is made of glass. Don't give excessive external shock, or drop from a high place.
- 2. If the LCD screen is damaged and the liquid crystal leaks out, do not lick and swallow. When the liquid is attach to your hand, skin, cloth etc, wash it off by using soap and water thoroughly and immediately.
- 3. Don't apply excessive force on the surface of the LCM.
- 4. If the surface is contaminated ,clean it with soft cloth. If the LCM is severely contaminated , use Isopropyl alcohol/Ethyl alcohol to clean. Other solvents may damage the polarizer . The following solvents is especially prohibited: water , ketone Aromatic solvents etc.
- 5. Exercise care to minimize corrosion of the electrode. Corrosion of the electrodes is accelerated by water droplets, moisture condensation or a current flow in a high-humidity environment.

6. Install the LCD Module by using the mounting holes. When mounting the LCD module make sure it is free of twisting, warping and distortion. In particular, do not forcibly pull or bend the I/O cable or the backlight cable.

7. Don't disassemble the LCM.

8. To prevent destruction of the elements by static electricity, be careful to maintain an optimum work environment.

- Be sure to ground the body when handling the LCD modules.
- Tools required for assembling, such as soldering irons, must be properly grounded.
- To reduce the amount of static electricity generated, do not conduct assembling and other work under dry conditions.
- The LCD module is coated with a film to protect the display surface. Exercise care when peeling off this protective film since static electricity may be generated.

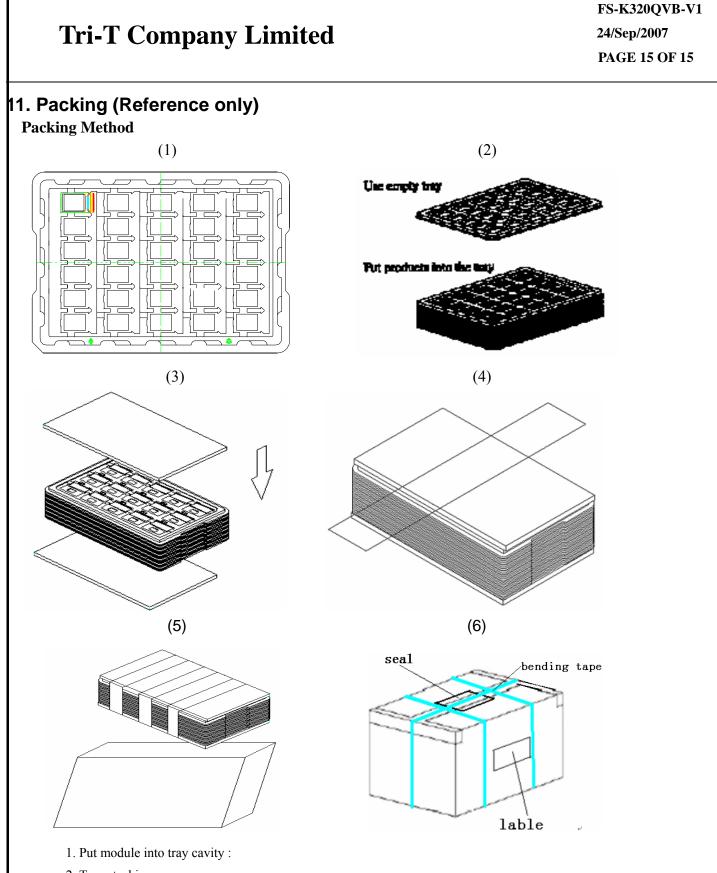
9. Do not alter, modify or change the the shape of the tab on the metal frame.

10. Do not make extra holes on the printed circuit board, modify its shape or change the positions of components to be attached.

- 11. Do not damage or modify the pattern writing on the printed circuit board.
- 12. Absolutely do not modify the zebra rubber strip (conductive rubber) or heat seal connector
- 13. Except for soldering the interface, do not make any alterations or modifications with a soldering iron.
- 14. Do not drop, bend or twist LCM.

10.2 Storage

- 1. Store in an ambient temperature of 5 to 45 °C, and in a relative humidity of 40% to 60%. Don't expose to sunlight or fluorescent light.
- 2. Storage in a clean environment, free from dust, active gas, and solvent.
- 3. Store in antistatic container.



- 2. Tray stacking
- 3. Put 1 cardboard under the tray stack and 1 cardboard above:
- 4. Fix the cardboard to the tray stack with adhesive tape:
- 5. Put the tray stack into carton.
- 6. Carton sealing with adhesive tape.

- END -