

MOD-USB-RS232 development board Users Manual



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INTRODUCTION:

MOD-USB-RS232 is module board with PIC18F14K50 microcontroller. It is a simple USB to UART converter that features UEXT. It can be used to add Virtual Com Port to other Olimex's module, or development board, with UEXT, but without Com Port. The board can be configured in both device and host mode, depending on jumpers 3_RX/3_TX and 4_TX/4_RX (see Jumper Description below). For using MOD-USB-RS232 with some of our modules, it's jumpers has to be configured for host mode. For using MOD-USB-RS232 with some of our development boards, it's jumpers has to be configured for device mode.

BOARD FEATURES:

- MCU: **PIC18F14K50** 20-Pin USB Flash Microcontrollers with nanoWatt XLP Technology.
- mini USB connector
- status led
- male UEXT connector plus 10 pin cable for it
- RESET circuit
- FR-4, 1.5 mm, red soldermask, component print
- Dimensions: 35.70 x 25.83 mm (1.41 x 1.02")

ELECTROSTATIC WARNING:

The **MOD-USB-RS232** board is shipped in protective anti-static packaging. The board must not be subject to high electrostatic potentials. General practice for working with static sensitive devices should be applied when working with this board.

BOARD USE REQUIREMENTS:

Cables: The cable you will need depends on the programmer you use. If you use PIC-ICD2-POCKET, or PIC-Kit3, you will need USB A-B cable. For PIC-ICD2-TINY, you will need RS232 cable. For PIC-ICD2, you will need USB A-B cable, or RS232 cable. You will need adapter <u>PIC-ICSP</u> for programming, because the board's ICSP connector is not Microchip's standard connector, it is a mini type one.

Hardware: Programmer <u>PIC-ICD2-POCKET</u>, <u>PIC-ICD2</u>, <u>PIC-ICD2</u>, <u>TINY</u>, PIC-Kit3, or other compatible programming tool.

Software: MPLAB IDE

Note: This microcontroller does not support debug functionality.

PROCESSOR FEATURES:

MOD-USB-RS232 board use USB Flash microcontroller **PIC18F14K50** from MICROCHIP Technology Inc. with these features:

- Flash Program Memory (words): 8k
- SRAM (bytes): 512
- Universal Serial Bus Features:
 - USB V2.0 Compliant SIE
 - Low Speed (1.5 Mb/s) and Full Speed (12 Mb/s)
 - Supports Control, Interrupt, Isochronous and Bulk Transfers
 - Supports up to 16 Endpoints (8 bidirectional)
 - 256-Byte Dual Port RAM (DPRAM) for USB
 - On-Chip USB Transceiver
- Flexible Oscillator Structure:
 - Four Crystal modes, including High-Precision PLL for USB
 - Two External Clock modes, up to 48 MHz
 - EC Low, Medium, and High-Power modes
 - Internal 31 kHz Oscillator
 - HF-INTOSC 16 MHz
 - Secondary Oscillator using Timer1 @ 32 kHz
 - Fail-Safe Clock Monitor:
 - Allows for safe shutdown if any clock stops
- Peripheral Features:
 - Three Programmable External Interrupts
 - Seven Interrupt-on-Change (IOC) Configurable for Rising, Falling or Both Rising/Falling Edge Detection
 - One Enhanced Capture/Compare/PWM (ECCP) module with Auto-Shutdown and PWM Steering
 - Master Synchronous Serial Port (MSSP) module supporting SPI mode, and I2C. mode with Address Mask Capability
 - Enhanced Universal Synchronous Asynchronous Receiver Transmitter (EUSART) module:
 - Supports RS-485, RS-232 and LIN compatibility
 - Auto-wake-up on Start bit
 - Auto-Baud Detect

- Analog Features:
 - 2 Analog Rail-to-Rail Comparator modules with:
 - Programmable rail-to-rail reference voltage (% of VDD) or 1.024V (nominal) band gap reference
 - Comparator inputs and outputs externally accessible
 - SR Latch mode
 - 10-Bit, 9-Channel Analog-to-Digital (A/D) Converter module
- Special Microcontroller Features:
 - Power-on Reset (POR)
 - Power-up Timer (PWRT) and Oscillator Start-up Timer (OST
 - Programmable Brown-out Reset (BOR)
 - Software enable feature
 - Configurable shutdown in Sleep
 - Low/high-power configurable
 - Four selectable trip points
 - Self-Reprogrammable under Software Control
 - Programmable Code Protection
 - Single-Supply In-Circuit Serial Programming. (ICSP.) via Two Pins
 - Operating Voltage Range of 1.8V to 5.5V
 - Priority Levels for Interrupts
 - Low-Power, High-Speed CMOS Flash Technology

BLOCK DIAGRAM:



MEMORY MAP:



SCHEMATIC:



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BOARD LAYOUT:



ICSP

POWER SUPPLY CIRCUIT:

MOD-USB-RS232 can take power from two sources:

5 VDC via pin holes VUSB and GND.

5 VDC via USB connector.

The programmed board power consumption is about 20 mA with all peripherals enabled

RESET CIRCUIT:

MOD-USB-RS232 reset circuit includes R1 (4.7k), R11(240 Ω /1%), D1 (BZV55C8V2), PIC18F14K50 pin 4 (RA3/MCLR/VPP).

CLOCK CIRCUIT:

Quartz crystal 12 MHz is connected to PIC18F14K50 pin 2 (RA5/OSC1/CLKIN) and pin 3 (RA4/AN3/OSC2/CLKOUT).

JUMPER DESCRIPTION:

USB_PWR_3.3



This jumper, when is closed, connects PIC18F14K50 pin 17 (VUSB) – signal USB_VBUS, to VCC (3.3V). Default state is opened.

UEXT_PWR_3.3



This jumper, when is closed, connects UEXT pin 1 to VCC (3.3V). When MOD-USB-RS232 is used in host mode – this jumper must be closed, because the other module will be power supplied via MOD-USB-RS232. Default state is opened.

3_RX/3_TX



This jumper, when is in position 3_RX, connects UEXT pin 3 to PIC18F14K50 pin 12 (RB5/AN11/RX/DT). When this jumper is in position 3_TX, connects UEXT pin 3 to PIC18F14K50 pin 10 (RB7/TX/CK).

Default state is 3_RX shorted.

4_TX/4_RX



This jumper, when is in position 4_TX, connects UEXT pin 4 to PIC18F14K50 pin 10 (RB7/TX/CK). When this jumper is in position 4_RX, connects UEXT pin 4 to PIC18F14K50 pin 12 (RB5/AN11/RX/DT).

Default state is 4 TX shorted.

In default state jumpers $3_RX/3_TX$ and $4_TX/4_RX$ are configured for device mode.

INPUT/OUTPUT:

Status Led (red) with name **STAT** connected to PIC18F14K50 pin 14 (RC2/AN6/P1D/C12IN2-/CVREF/INT2).

EXTERNAL CONNECTORS DESCRIPTION: UEXT:

Pin #	Signal Name
1	VCC
2	GND
3	RXD
4	TXD
5	SCL
6	SDA
7	MISO
8	MOSI
9	SCK
10	#SS



<u>USB</u>

Pin #	Signal Name
1	USB_POWER
2	D-/PGC
3	D+/PGD
4	Not Connected
5	GND



MECHANICAL DIMENSIONS:



AVAILABLE DEMO SOFTWARE:

• USB to UART converter with Windows drivers for Serial Port

ORDER CODE:

MOD-USB-RS232 - assembled and tested board

How to order?

You can order to us directly or by any of our distributors. Check our web <u>www.olimex.com/dev</u> for more info.

Revision history:

Board's Revision:Rev. A, created May 2010Manual's revision:Rev. Initial, created May 2011

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